Amendments to the Abstract:

Please replace the Abstract with the following replacement Abstract:

A low voltage wide ratio current mirror circuit comprises an n times current mirror having an input port for receiving an input current and an m times current mirror coupled in series to the n times current mirror for resulting in an output current of (N*M the input current) being provided to a load where at least one of N and M is other than 1. The novel circuit provides significant improvements in precision in output current for use with a low voltage power amplifier without incurring an overhead of quiescent current. The low voltage wide ratio current mirror circuit in accordance with a second embodiment of the invention includes a voltage swing reduction circuit in order to provide increased stability thereto. In additional embodiments of the invention, the load is a differential amplification stage for providing differential amplification to differential RF input signals received at first and second RF input ports thereof.